



Sarvajanik Education Society  
**Sarvajanik College of Engineering & Technology**  
Faculty of Electronics and Communication Engineering



**A Report on Interaction with Alumni on  
Career in ASIC Design**

**Organized by:**  
Faculty of Electronics and Communication Engineering

**Alumni:**  
Mr. Mukesh Swain

ASIC engineer, Juniper Technologies  
On  
**11<sup>th</sup> February 2019**

Faculty of Electronics and Communication Engineering organized an interactive session under the banner of “Share What I Gain”, initiated by SCET Alumni Association for the Students of SCET on **11<sup>th</sup> February 2019**. The purpose of the interaction was to address the UG Students regarding job opportunities, career counseling and personal experiences in the era of development and technology. The interaction was based on improvement in day to day curiosity and problem-solving skills for achieving ones childhood dreams

### **About The Alumni**

The alumni guest was Mr. Mukesh Swain currently working as **an ASIC engineer, Juniper Technologies, San Francisco, United States, a 2015 alumnus of Electronics and Communication Engineering Department**. Mukesh, himself went for his graduate studies at University of Texas at Dallas, which is a renowned university in the United States, especially renowned for its courses in Electrical Engineering. He is been really successful in the field on VLSI and ASIC Design.

### **During Interaction**

The guest lecture was organized at 10 A.M. and Mukesh started it off by introducing himself. Students asked multiple doubts related to the career options after under graduation and also job opportunities in VLSI design. Students also asked questions regarding pursuing MS in the US. It was a very interactive session and Mukesh answered all the questions in a concise manner and elucidated every point he had said. In the course of this interactive discussion, students came to know about a lot of specialization and fields in VLSI domain such as digital design, design verification, ASIC Design, FPGA Development, SoC design etc. The fields and specialization enlisted by him are new and emerging to the industry and have exorbitant demand for engineers having knowledge of VLSI.

He was of the view to pursue further studies after under graduation and briefly described about the opportunities available to work in the field of VLSI. Students asked him about his experience at University of Texas at Dallas, and he briefly explained that. He told about the subjects he studied there and shared what he got to know about the western education system where students have the liberty to choose the subjects they want to study while also satisfying the credit rule. Not only for abroad education, had Mr. Swain also highlighted importance and opportunities in India.

Prof. (Dr.) Chirag Paunwala also suggested some ideas such as Mukesh mentoring students as an external guide for their projects. Mukesh was very keen to help and we all were glad to have him here.

To conclude, this session was very informative as students got a lot to learn from Mukesh. Also there are many things which could not be taught in the class such as the opportunities available after under graduation or the job opportunities available in the various fields of our domain such as VLSI and embedded systems.

We are very thankful to Mukesh for spending some valuable time from his busy schedule to enlighten us with the best of knowledge he had gained from his experiences. Participation was from BE III year students who enthusiastically participated in the interaction and expected such events more.

We are thankful to Dr. Vaishali Mungurwadi, Principal SCET as well as Prof. (Dr.) Maulin Joshi, Head of Department, Faculty of Electronics and Communication for providing their valuable guidance and support.

Few glimpse of the session:



