Sarvajanik College of Engineering and Technology

Department of Computer Engineering

Academia-Industry Meet 2025 (AIM 2025)
in association with
R & D Cell, IIC and SSIP Cell

Objectives of the Talk:

- To make students well-aware about the industry techniques and technologies from various esteemed industry experts.
- To bridge the gap between theoretical knowledge in academia and practical applications in industry./
- To make students aware about the skills they should develop to be industry ready.
- To explore partnership opportunities between academia and industry to promote joint projects, research and innovation.

Date & Day: 18th March 2024 (Tuesday)

Time: 11:00 AM to 2:00 PM

Venue: EC-AV Room

Attendees: 86 attendees (80 students of BE III Computer Engineering Department, and 6 faculty members)

Speakers:

Mr. Anshu Pandey, Head of Technology and Consultant, Blue Data Consulting, Surat
Mr. Arjun Kava, CEO at Video SDK, Bangalore
Mr. Shubham Agarwal, Co-Founder and COO, GameAshler, Surat
Mr. Jignesh Patoliya, Manager, Einfochips Ltd., Ahmedabad

Coordinator: Prof. (Dr.) Mayuri Mehta, Department of Computer Engineering
Prof. (Dr.) Nirali Nanavati, Department of Computer Engineering

Key Takeaways:

- Generative AI and Cloud Advancements – Mr. Anshu Pandey highlighted how cloud computing is making AI more accessible, with generative AI transforming workflows into prompt-based operations. He emphasized critical thinking, adaptability, and continuous learning for future tech professionals and encouraged the students to be problem solvers.
- Voice and Vision Interfaces - Mr. Arjun Kava discussed the shift to voice and vision interfaces, driven by AI-native technologies like Kubernetes and multimodal indexing, enhancing real-time digital worker ecosystems.
- AR/VR Innovations – Mr. Shubham Agrawal explained AR/VR applications across industries, demonstrating real-world use cases and challenges. He emphasized the future impact of the metaverse, AI-powered AR/VR, and 5G.
- AI and Semiconductors – Mr. Jignesh Patoliya detailed the role of semiconductors in AI

advancements, discussing GPUs, high-compute ASICs, and industry trends, along with key technologies like FPGA, DFT (Design for Testability), and PD (Physical Design) for chip design.

Summary of the Event:

The Department of Computer Engineering organized the Academia-Industry Meet under R&D Cell, SCET in association with SSIP Cell & IIC Cell of SCET, Surat on 18th Mar 2025. The SCET AIM-2025 brought together a diverse group of developers, engineers, and industry experts to discuss the latest trends, technologies, and challenges in the software development landscape.

The AIM featured expert speakers' presentations, networking opportunities, panel discussion and knowledge sharing among participants.

The AIM 2025 began with the introduction and felicitation of the speakers by the faculty members. The first speaker, **Mr. Anshu Pandey, Lead at Blue Data Consulting**, shared insights on "AI and Cloud: Today & Tomorrow." He highlighted how cloud computing is driving technological growth by offering cheaper data storage and enabling ML solutions without extensive infrastructure. He emphasized that cloud is becoming as common as MS Office in IT, making it no longer a specialized skill. Mr. Pandey also discussed the rise of Generative AI, where data collection, preparation, and deployment have transformed into prompt-based operations post-2022. He mentioned Deepseek's $6 million USD (~INR 51 crore) investment in AI, with 5-20% of tech spending now dedicated to generative AI. He concluded by advising students to focus on becoming architects, enhancing critical thinking, collaboration, adaptability, and continuous learning to thrive in the evolving tech landscape.

The second speaker, **Mr. Arjun Kava, CEO at Video SDK**, spoke on the topic "Developer's Role in Building Real-time Human Digital Worker Ecosystem." He emphasized the shift towards voice and vision interfaces, moving beyond traditional CLI and GUI systems. Mr. Kava discussed how cloud and AI technologies, powered by Kubernetes, Docker, and scalable microservices, are driving the creation of AI-native digital workers. He highlighted the role of real-time OS with interfaces integrating voice, vision, and action, supported by vector-based multimodal indexing and powered by LLMs, VLMs, and LAMs, making digital interactions more seamless and intelligent.

Further, **Mr. Shubham Agrawal, Co-founder and COO of GameAshlar**, spoke on the topic "AR and VR." He explained that Augmented Reality (AR) enhances the real world by overlaying digital elements, while Virtual Reality (VR) creates an entirely immersive virtual environment. Mr. Agrawal highlighted the growing market trends of AR/VR across industries, including gaming, education, healthcare, and real estate. He shared practical applications, such as lipstick and clothing filters in AR and how companies like Swiggy use AR-powered ads. He showed various interesting demos regarding different AR/VR based games and applications. He also discussed the challenges of hardware limitations in AR/VR adoption. Looking ahead, he emphasized the impact of metaverse and spatial computing, AI-powered AR/VR, 5G and edge computing and the emergence of mixed reality, which blends physical and digital environments, creating more interactive and immersive experiences.

The last speaker of the summit was **Mr. Jignesh Patoliya, Manager at eInfochips Ltd., Ahmedabad**, who spoke on the topic "Impact of AI on Human Daily Life Through Advancement of Semiconductors." He highlighted how AI-powered applications such as self-driving cars, language translation, financial analysis, human-robot interactions, and healthcare are transforming daily life. He explained that the backbone of AI is semiconductors, with GPUs enabling parallel processing, making AI computations faster and more efficient. Mr. Patoliya discussed industry trends, mentioning the rise of high-compute ASICs for AI/ML and

the projected growth of the AI semiconductor market to $227.48 billion by 2032. He also introduced SystemVerilog, a hardware description and verification language used in semiconductor design, highlighting how computer engineers can transition into the semiconductor industry. Lastly, he briefly introduced key technologies like FPGA (Field-Programmable Gate Array), DFT (Design for Testability), and PD (Physical Design), which are essential for building and optimizing semiconductor chips.

In the end, we had an engaging question and answers and discussion session between students and speakers, where speakers solved the student's questions with their profound knowledge and industrial expertise.

We extend our sincere gratitude to our honorable principal Dr. Hiren Patel for permitting us to organize this talk. His constant support and guidance inspire us to organize such enlightening events for our fellow students and colleagues. We are also thankful to Dr. Utpal Pandya, Dean, R&D and Dr. Dipali Kasat, HOD, Department of Computer Engineering for encouragement and prompt support always. We extend our earnest gratitude to all the speakers for accepting our invitation and sparing their valuable time to be with us for the Summit. Sincere thanks to all the student volunteers, lab assistants and lab attendants for making necessary arrangements for this session. Thanks to all staff members of the Computer Engineering department for extending their help in all the possible ways whenever required. Last but not the least, we are very thankful to all the participants for their active presence during the session.

Photos of the event: