

Sarvajani Education Society
Sarvajani College of Engineering & Technology
Dr. R. K. Desai Marg, Opp.Mission Hospital, Athwalines, Surat-395001

Report on
“Demo Day/ Idea Showcase”

Date: 18th and 20th November 2025

Venue: EC AV Room, SCET

Faculty Co-ordinators

Dr. Nehal Shah

Dr. Vandana Shah

In association with
Institution's Innovation Council (IIC), SCET

Introduction

The Electronics and Communication Engineering Department of Sarvajanik College of Engineering and Technology (SCET), Surat, in association with the R&D Cell, SCET, successfully organized the State Level IoT Design Challenge 3.0. The event was conducted with the objective of encouraging innovation, creativity, and practical implementation skills among engineering students in the rapidly evolving domain of the Internet of Things (IoT). The competition witnessed enthusiastic participation from students across the state, with more than 75 project entries, reflecting the growing interest and relevance of IoT technologies in solving real-world problems.



The poster is blue with white and yellow text. At the top, there are three logos: Sarvajanik College of Engineering and Technology (SCET), IETE, and a circular logo for the Electronics and Communication Engineering Department. Below the logos, the text reads: "Electronics and Communication Engineering Department (Re-Accredited by NBA, New Delhi for 3 years, W.E.F 1 July 2022) Sarvajanik College of Engineering and Technology, Surat In association with R&D cell, SCET Organizes STATE LEVEL IOT DESIGN CHALLENGE 3.0". To the left of the title is a lightbulb icon with a circuit inside. Below the title, the rules are listed: "RULES: • PARTICIPATION IS IN A GROUP • MAX. 4 STUDENTS/GROUP • POSTER MUST BE PREPARED FOR PRESENTATION COMPRISES OF GROUP DETAILS, BLOCK DIAGRAM, BRIEF EXPLANATION AND OTHER TECHNICALITIES". In the bottom left, the faculty coordinator is listed: "FACULTY COORDINATOR: DR. NEHAL SHAH DR. VANDANA SHAH". Below that, the head of department is listed: "HEAD OF DEPARTMENT: PROF. (DR.) CHIRAG PAUNWALA". In the bottom left, the student coordinator is listed: "STUDENT COORDINATOR: KHUSHI DESAI (9824125846) JHARNA NAKRANI (9429990594)". In the bottom right, the date and time are listed: "18TH & 20TH NOVEMBER, 2025 TIME: 09:00 AM- 11:00 AM VENUE: EC AV ROOM". A large QR code is in the center, framed by a circuit board design.

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About Design Challenge:

The IoT Design Challenge 3.0 was a state-level technical event designed to provide students with a platform to showcase their innovative IoT-based solutions. Participants presented their ideas through well-structured posters comprising group details, block diagrams, technical explanations, and application-oriented concepts. The event emphasized hands-on learning, interdisciplinary thinking, and exposure to current industry and research trends in IoT.

The challenge was conducted on 18th and 20th November 2025, from 09:00 AM to 4:00 PM, at the EC AV Room, SCET, Surat.



Participation Details:

Participation was allowed only in groups

Each group consisted of a maximum of four students

A poster presentation was mandatory, including:

Group details

Block diagram

Brief explanation

Technical specifications and applications

The event received 75+ entries, indicating strong student engagement and competitive spirit.

Event Highlights:

Inauguration Ceremony:

The event commenced with a formal inauguration in the presence of faculty members, coordinators, jury members, and participating students. The organizers highlighted the

importance of IoT in modern engineering applications and its role in Industry 4.0, smart systems, and sustainable development.



Keynote Address by Jury:

Dr. Nehal Shah and **Dr. Vandana Shah** delivered an insightful keynote address emphasizing the **importance of IoT and its diverse applications** across domains such as healthcare, smart cities, agriculture, automation, and environmental monitoring. They also guided students on:

- Enhancing project innovation
- Improving design methodology
- Aligning projects with real-world applications
- Identifying scope for future improvements and commercialization

The address motivated students to think beyond academic requirements and focus on practical implementation and societal impact.

Interactive Session

An interactive session was conducted between the **jury members and participating students**, where each group received constructive feedback. The discussion focused on:

- Scope for improvement
- Technical feasibility
- Design optimization
- Future development possibilities

This interaction helped students gain valuable insights into project refinement, research orientation, and professional presentation skills.



Conclusion:

The **IoT Design Challenge 3.0** was successfully conducted and proved to be an enriching experience for all participants. With **over 75 project entries**, the event highlighted the creativity, technical competence, and problem-solving abilities of students. The expert guidance, interactive evaluation, and structured presentation format significantly contributed to students' academic and professional development. Overall, the event strengthened practical learning, fostered innovation, and reinforced the importance of IoT technologies in modern engineering education.
