Sarvajanik College of Engineering and Technology, Surat
Electronics and Communication Engineering Department
Re-accredited by National Board of Accreditation, New-Delhi
for
3 years, w.e.f. July 2019







A Report on State Level Project Competition on On On "Digital Electronics"

Date: 29th November 2021

Venue: EC Dept. Laboratories, SCET

Faculty Coordinators: Asst. Prof.Chhaya Suratwala

Asst. Prof. Chintan Panchal

Head of Department: Prof. (Dr.) Nehal Shah

Electronics & Communication Engineering Department, SCET-Surat in association with IETE Surat Sub center along with ESRO- Embedded System and Robotics Club, SCET had organized a one day State level project competition on "Digital Electronics" on 29th November 2021. IETE Surat Sub Centre has been the sponsoring entity for the prize money to be given to winners. The competition was arranged for the students of 1st, 2nd and 3rd year of any branch across any engineering colleges of the state of Gujarat. There was no registration fee to participate. The event was arranges in offline mode. The objective of technical competitions is to introduce students to real-world engineering situations, to enhance project-management and Design techniques used in industries. Student are encouraged to work on hardware using Combinational/Sequential/CPLD/FPGA. They allowed to present project in group of two or at max four.

Total 24 groups have registered and 22 groups participated in the event. Many group registered themself as spot entry. A group consists of (4-2) students that make total individual students to around 72 participated in the competition. Various projects like AGVM (Automated Guided Vehicle Machine), smart parking management, universal shift register using FPGA, Automatic door lock system, Article Enumeration System, Water Level Indicator, Object Counter, Digital 12hrs clock, Automatic Night light, Electronics die ,traffic light control were demonstrated by students. The hard work and dedication of the students were reflected in results of the projects they have made. All the projects were evaluated by eminent juries from industry named Mr. Bhaumik vaidya owner Project, Technoink, Surat having vast experience in the machine learning and computer vision ,digital electronics, Hardware FPGA,CPLD, ML and AI systems. Few EC Dept staff SCET were also present as internal Jury.

List of Winners:

Winners Name	Project Title	Rank
Sarthak Bosamiya, Jhanvi Bhalodia, Jeel Ramani, Harshita Agrawal	Parking Management System	1 st
Deepak Shamsheer, Niket Singla, Aleemuddin Mombasawala, Rahul Agarwal	Traffic light control system using CPLD	2 nd
Neel Khanpara, Ansh Dobariya, Shivang Vyas, Aarya Lokhandwala	AGVM- Automated Guided Vehicle Machine	3 rd
Rishi Joshi, Dhaval Sorathiya, Priyam kakadiya, Janvi Nariya,	Digital 12 Hour Clock	Consolation

.

Project "Parking Management System" is demonstrating weather parking at -2,-1 0 is full or have some lot for allotment. That information is displayed outside parking place. Boom barrier on place get operated on the basis of lot for parking .This is demonstrated with the help of servo motor, motion sensor and Flip flops and basic gates. Three groups were announced as winner on 1st, 2nd and 3rd position collectively by both the juries. one consolation winners were also announced. All participants were given e-certificate of participation and winners were awarded with certificate and prizes as which were sponsored by the IETE SCET SB

The day was ended well by taking participants feedback whereby students shared their experience of participation in the competition and were really very happy to gain knowledge in this domain. Participants also insisted to organize such kind of competitions in future also. Overall it was fruitful experience for all participants and coordinators.

Jury 1:

Mr. Bhaumik Vaidya, Senior Software Developer (AI/ML), Mobile Programming LLC. He is an experienced Machine Learning engineer and mentor with over eight years of experience. He has worked extensively on deep learning algorithms for solving computer vision problems. He is a University gold medalist in masters and is now doing a PhD in the acceleration of computer vision algorithms built using OpenCV and deep learning libraries on GPUs. He has mentored many projects in computer vision and VLSI(Very-large-scale integration). He is also working with a startup in development of efficient and scalable solutions using Artificial Intelligence. He has worked in the VLSI domain previously as an ASIC verification engineer, so he has very good knowledge of hardware architectures also. He has published one book titled "Hands on GPU accelerated computer vision using OpenCV and CUDA." He has more than dozen publications in high quality journals and conferences in the field of computer vision and machine learning. He, along with his PhD mentor, has also received an NVIDIA Jetson TX1 embedded development platform as a research grant from NVIDIA.

Participation status college and branch wise:

Year wise

1st	8
2 nd	64
Total	72

Branch wise

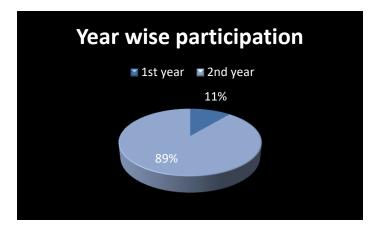
EC	57
CO	15
Total	72

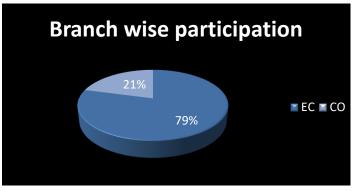
College wise

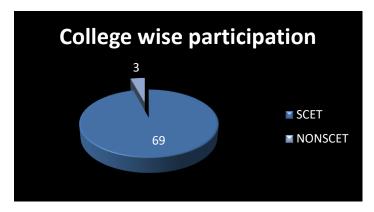
SCET	69
NON SCET	3
Total	72

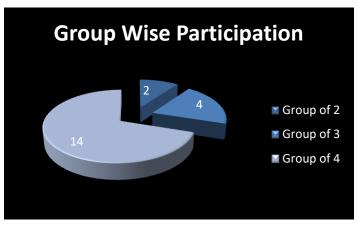
Group wise

Group of 2	2
Group of 3	4
Group of 4	14
Total	72









Few Glimpse of the Competition:

















State level Project Competition On Digital Electronics

Rules

- Participation in group.
- Group of 2 4 students.
- Project components must be managed by students.
- Laboratory resources like power supply bread board, DSO, etc will be provided.
- Strictly based on hardware using Combinational/Sequential.
- CPLD/FPGA board can be used.
- Open for 1st ,2nd and 3rd Year students from all branches.

Organized by

Electronics and Communication Engineering Dept, Sarvajanik College of Engineering and Technology,(SCET)

Date: 29th November 2021
Time: 9 Am to 1 Pm
Venue: Laboratory ,EC
Dept.,SCET



Prizes

1st: 2000 Rs 2nd: 1500 Rs 3rd: 1000 Rs

Registration Link https://tinyurl.com/m4du2e8

Registration QR

(Coordinators)

Prof. Chhaya Suratwala (9727790930) Prof. Chintan Panchal (9909152281) (Committee members)

Prof. Neeta Chapatwala (9712993433) Prof. Dhiren Bhagat (9898299266)

Attendance:

eam No.	Email Address	Team Leader Name	Wemb	Team Leader Contact	Project Title	College Name	Branch	Year
1	hituseptember@gmail.com	Hiteshree V Varsale	er	9726686055	* U-1 *	COET	Other	1st
		Kushal maiwala	2	372090000	Nirbhaya	SCET		
	sarthakbosamiya32@gmail.com	Sarthak Bosamiya		9904373086			EC	1st
2		Jhanvi Bhalodia	1993		Smart Parking	SCET		
-		Jeel Ramani	4		Smart Farking	COLI		
		Harshita Agrawal						
	neelkhanpara.ec21@scet.ac.in	Neel Khanpara		9824756249			EC	1st
3		Ansh Dobariya	4		Line follower robot without	SCET		
		Shivangi Vyas			microcontroller			
-	W 1 1 00110 1 1	Aarya Lokhandwala					-	0.1
4	pritamdavda.co20d1@scet.ac.in	Pritam Davda	- 2	7359487834	Digital Technology	SCET	CO	2nd
		Hetvi Jariwala			9 91			
	ruchitsheta.ec20@scet.ac.in	ruchitsheta.ec20@scet.ac		6355689903			EC	2nd
5		.in Hema Sen	4		Traffic light	SCET		
3		Maitri Desai	4		- Traine ight	JOLI		
		Kenil Faldu						
	roshansah.co20d1@scet.ac.in	Roshan Sah		7228845169		ight SCET CO		2nd
2	103/18/138/1.002001@3001.80.111	Nehul Vasava		72200 10 100	Automatic Night light			
6		Aniket Patel	4					
. 7	Y	A B Utkarsh Sorathia						
	parthkunjadiya.co20d1@scet.ac.in	Kunjadiya Parth vallabhbhai		9510554054			СО	2nd
119		Vaghani rushal vrajlal			Encryption and Decryption	SCET		
7		Golakiya Akshay Sanjaybhai	4		with error detection			
		Baldaniya Jignesh Rameshbhai						
	ibrahim.ghasia@gmail.com	Ibrahim Ghasia		8866685052	The second secon	SCET	СО	2nd
8	atte:	Rishil Shah	4		LED Race			
		Siddh Mandirwala		The second second	(AB)			
		Dharmil Shah		8758599684				Ond
	meetkathiriya.ec20@scet.ac.in	Meet kathiriya		0/00099084			EC	2nd
9		Gautam vaitani	4		Clod drinks vending machine	SCET		
		Shweta patel Virai kansara	1					

State Level Project competetion on "Digital Electronics"

Date: 29 Nov 2021

lo.	Email Address	Team Leader Name	Memb er	Team Leader Contact	Project Title	College Name	Branch	Year
	niketsingla.ec20@scet.ac.in	Deepak Shamsheer		6359592927	Traffic light control system		EC	2nd
10		Niket Singla				SCET		
		Aleemuddin Mombasawala	4		Traine light control system	0021		
	1	Rahul Agarwal						
	nimeshkumarsarvaiya.ec20@scet.a c.in	Ayushi bhatiya		7575835552	a to design and a 9 hit		EC	2nd
11		Sarvaiya Nimeshkumar Sureshbhai	4		Design and implement a 8-bit universal shift register and	SCET		
		Nit talavia			show results on an FPGA.			
	THE RESERVE OF THE PARTY OF THE	Hitaxi lethwala						
	Priyanshigelani.ec20@scet.ac.in	Priyanshi gelani		9313245258			EC	2nd
12	ABjanvishah.ec20@scet.ac.in	Jawi shak			will be open . Which cab be shown using LED .	SCET		
	kandarpdarji.ec20@scet.ac.in	Kandoup dougi	4	777882226				
	hetasvibhimani.ec20@scet.ac.in	Hotasvi phimani		8154970909				
	sanskrutipatil.ec20@scet.ac.in	SANSKRUTI PATIL		9104120112	Design and implement a	SCET	EC	2nd
	adrian di patin et 20 de acet ac.in	Sarangi kothadiya	4		Tea+coffee vending machine			
13		Mohil Jain	4		using verilog program.			
		Soshmita paul			using verilog program.			
	aaryashah.ec20@scet.ac.in	Aarya Shah		9979941905	Temperature sensing and	SCET	EC	2nd
14		Harsh Bhadani	4		displaying using Im35			
		Ishaq Malam			anopaying comg innoc			
		A B Marmik Joshi						2-1
	jhawarmusu1509@gmail.com	Muskan Jhawar	-	7202095213			EC	3rd
15	The state of the s	Abanob Bhanu	4		Smart door system	SCET		-
0		Saglain Shaikh	-		AR			
		Chavi Kothari		040070000	113		CO	2nd
	smitpansuriya12@gmail.com	Smit Pansuriya	-	8469700661	There are a second seco	H 7 7 7 1	00	1 2110
16	The state of the s	Jenish vaghasiya	4		Password management using	SCET		
10		Krisha Bhingradiya	-		4 bit comparator			
		Priyanshi kalsariya						

State Level Project competetion on "Digital Electronics"

Date: 29 Nov 2021

No.	Email Address	Team Leader Name	Memb er	Team Leader Contact	Project Title	College Name	Branch	Year
-	rishijoshi.ec20@scet.ac.in	Rishi Joshi		9016234311			EC	2nd
17		Dhaval Sorathiya	4	7990065795	Digital 12hrs clock from logic	SCET	EC	2nd
		Priyam kakadiya		9016094332	10		EC EC	2nd 2nd
	anandsahu.ec20@scet.ac.in	Janvi Nariya		7990293156				
	anandsand.ecz0@scet.ac.in	Abhishek Singh		84609 32364	Generate a sequence		EC	2nd
18		Meet patel	4		generator which generates	SCET		
Ì		Anish sopariwala Anand Sahu		-	the sequence(101).			
19 <u>ka</u>	kashishpatel1262@gmail.com	Kashish Ketankumar Patel		7878478971	Article Enumeration System	Dr. S & SS Ghandhy	EC	2nd
	er.yashpawar@gmail.com	Yash Anilbhai Pawar	Owarkesh 6352073088	7016439736		Government	EC	2nd
	divyangparekh625@gmail.com	Divyang Dwarkesh Parekh			Engineering College	EC	2nd	
20	utsavnavadiya.co20d1@scet.ac.in	Parth Kalathiya	2	8401830803	Object Counter	SCET	со	2nd
20		Navadiya Utsav	-		object obanici	SCET CO	2nd	
	deeppatel.ec20@scet.ac.in	Deep Patel		9327454629	Water Level Indicator	SCET	EC	2nd
	nehanshpatel.ec20@scet.ac.in	Nehansh Patel	4	9054165165		SCET	EC	2nd
21	jeetmehta.ec20@scet.ac.in	Jeet Mehta		7567911880		SCET	EC	2nd
	jaytaneja.ec20@scet.ac.in	Jay Taneja		9723873082		SCET	EC	2nd
	jillsaliya.ec20@scet.ac.in	Jill Saliya	-	9924745599 9824287721		SCET	EC	2nd
	# Annual Control of Co	Krishi Modi	4	9327805584	Parking Slot	SCET	EC	2nd
22		Nirjara Vasanwala		8154002600		SCET	EC EC	2nd 2nd
		Dhanisth Sarawagi		9033124997		SCET	EC	2nd
19 1	devanshthakkar.ec20@scet.ac.in	Devansh Thakkar	1	9104000501		SCET	EC	2nd
-	khushvipatel.ec20@scet.ac.in	Khushvi Patel	- 4	9327305774	4-BIT MULTIPLIER	SCET	EC	2nd
23	aasthapatel.ec20@scet.ac.in	Aastha Patel		9723314258	HITALE WATER STREET	SCET	EC	2nd
	prathampatel.ec20@scet.ac.in	Pratham Patel		9512494969		SCET	EC	2nd
	dharmarajjardosh.ec20@scet.ac.in	Dharmaraj Jardosh	1 4	8849887048	4 bit universal abit	SCET	EC	2nd
-	devpatel.ec20@scet.ac.in	dev patel	7 4	9313457910	4 bit universal shift register	SCET	EC	2nd
24	mohitbhatia.ec20@scet.ac.in	mohit bhatia		9510455407		SCET	EC	2nd

