



Sarvajani Education Society

## **Sarvajani College of Engineering and Technology**

**Department of Electrical Engineering**



### **A Report on Interaction with an Alumnus on Tricks and tactics for GATE preparation**

**Organized by:**  
**Department of Electrical Engineering**  
SCET Alumni Association

**Alumnus:**  
**Mr. Parth Bapaye**  
Embedded Design Engineer at Spectrum, Surat  
On  
24th September '2018

**Co-ordinator:**  
**Prof. Urvi T. Jariwala**  
**Prof. Ekta J. Desai**  
(SCETAA Representatives, Electrical Department)  
**Prof. Dimple T. Bhanabhagwanwala**

Department of Electrical Engineering organized an interactive session under the banner of “**Share What I Gain**”, initiated by SCET Alumni Association for the 3<sup>rd</sup> year and final year students of Electrical Department, SCET. The talk was imparted by Mr. Parth Bapaye, ‘2013 alumnus. The purpose of the session was to guide students about GATE preparations.

### **About the Alumnus**

The invited worthy Alumnus is currently working as a Design Engineer in Spectrum Solutions and Technologies Pvt Ltd. Surat.

After graduation he joined Bitmapper Integration Technologies Pvt Ltd as a Design Engineer in June 2013. In 2015 he got admission in Indian Institute of Technology, Bombay (IIT, Bombay). His M.Tech. project is in Signal Processing and Hardware Development for Wearable Health Care Devices under the guidance of Prof. Maryam Shojaei Baghini. He had worked and accomplished many course projects in Embedded system and VLSI. He successfully completed his masters with 9.01CPI in June 2017.

Currently in Spectrum Solutions and Technologies Pvt Ltd., he has designed and developed the modern electronic jacquard controller, and aided in the development of jacquard as a product for the company.

Spectrum Solutions and Technologies Pvt Ltd. is a Surat based company engaged in design, development and manufacturing of various products. All the required areas of embedded electronic, software, network based real time software and mechanical units are designed and manufactured in house.

### **Highlights of the interaction:**

- Mr. Parth shared his journey from SCET to Indian Institute of Technology, Bombay.
- He highlighted how to cleverly prepare and crack GATE examination to fetch admission in IITs for masters.
- He discussed some experiences of his batch mates and juniors to prepare for GATE examinations.
- He shared some technical fundamentals and tricks for the questions in Electrical Engineering, frequently asked in GATE exams.
- It was an interactive session, many final year students asked him many doubts.
- He also showed a video of an electronic jacquard machine designed and manufactured in Spectrum and explained its operation.

Here are a few glimpses of the session:

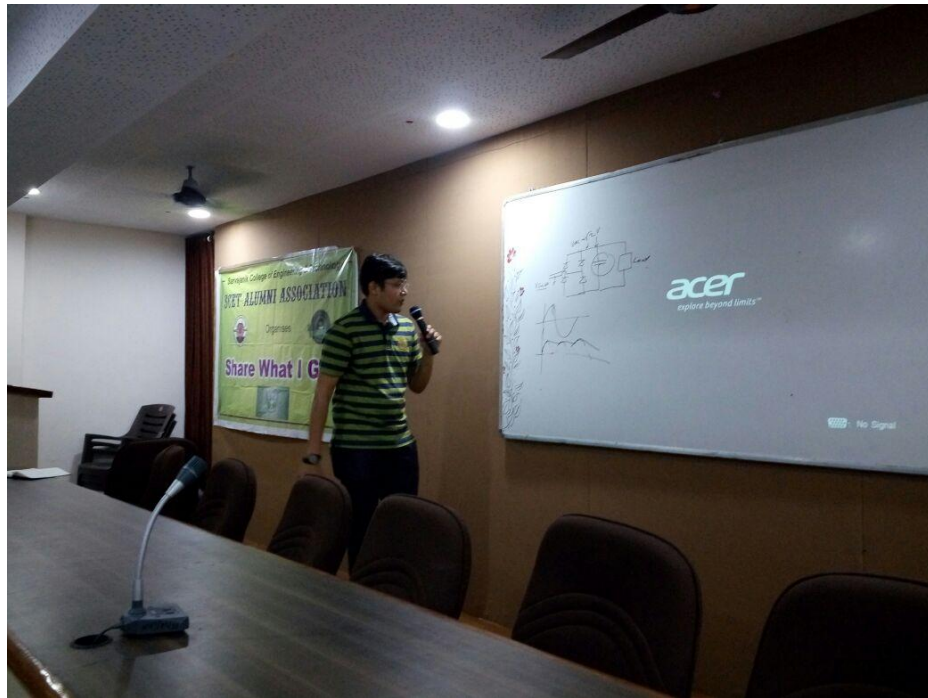


Mr. Parth delivering the talk



Interaction with the students





Mr. Parth explaining technical fundamentals



Mr. Parth welcomed by Prof. Urvi T. Jariwala



Mr. Parth and team members being thanked by Prof. Ekta J. Desai



Mr. Parth being presented a memento by Prof. Dimple T. Bhanabagyanwala