



Sarvajanik University
Sarvajanik College of Engineering and Technology [SCET]
B.Tech Electrical Engineering [SCET]
S-2023 Backlog Exam Semester-III Reassessment Result



Notification : SU/EXAM/ResultNotification/S2023Bklg_Re-Assessment_11082023-012

Print On Date: 11/Aug/2023

Notification : SU/EXAM/Result/Notification/S2023BRig_Rc-Assessment_11062023-012																																											
No. of Credit			2					4					5					4					4					4					0					Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Principles of Economics and Management[BTAS10301]					Applied Mathematics for Electrical Engineering[BTAS11303]					Electrical Circuit Analysis[BTEL13304]					Engineering Electromagnetics[BTEL13302]					Analog and Digital Circuit[BTEL13306]					Electrical Machine - I[BTEL13301]					Essence of Indian Traditional Knowledge[BTMD17301]										
			CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade						
1	ET22BTEL805	Jaykumar Chandreshbhai Patel	BC		FF		FF	CC	BC	CD	CD	CC	CD	BB	FF	CD	FF	BC		FF		FF	CC	BC	CD	AA	BC	AB	AA	FF	BB	FF			CC		CC	52	23	2.26	8	4	Fail

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NOTE: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment later.

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