

## Sarvajanik University

Sarvajanik College of Engineering and Technology [SCET]
Bachelor of Technology in Computer Engineering [SCET]
S-2023 Backlog Exam Semester-I Reassessment Result



Print On Date: 29/Aug/2023

Notification ·	SU/EXAM/ResultNotification/S2023Bklg_	Re-Assessment	29082023	002
Nounication :	SU/EXAMINATION SECTION	THE IMPOSSIBLE		

No. of Credit 4			4 3							3				4					3					2				2				3 Technical				Current	Total credits	,	Earned	ed s Currei	nt
		TI	athematic BTAS1110	21	Ph	veicel	gineerin [BTAS1	11051	Me	chani	iginee	CL12	2113]	Engi	ineeri	c Electr ng[BTI	EL121	08] E	Engine	asic M eering	BTM	E12109]		Engineer	thics for rs[BTAS]	10102]	1	ractice[B		-	_	ımuni	cation	[BTA	S10101]	- Grauc	in	Sem SP	nt PI in	sem	Resu
Sr No Enrollment No Student Name	CAT CA	P TEE TE	Cour Grad	se CAT C	AP 1	TEE TE	P Cour Grad	se e CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE T	EP Co Gr	urse ade C	AT C	AP TE	E TE	P Cours Grad	e CA	AT CAP	TEE TE	P Course Grade	CAT	CAP TE	ЕТЕР	Cours Grade	CAI	САІ	TEE	TEP	Course Grade	points	sem	1	sem	DKIG		
1 ET21BTCO134	Vasava		FF	FF	BB																1	1	1					AB	- 1	AB		1			1	136	22	6.18	18	1	Fail
2 ET22BTCO013	Chaudhari Abhipsa Dipakbhai	CD	CD	CD	BC	AB	CD BO	ВС	СС	AB	CD	AA	ВС	CD	AA	FF B	BB I	FF	CC E	BB C	D BI	BC BC	C	С	CD	CC		AB	AB	AB						113	21	5.38	17	1	Fail

Notification: SU/EXAM/ResultNotification/S2023Bklg\_Re-Assessment\_29082023\_002

NOTE: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment later.

B

Prepared by & Checked by, Mr.Hardik D. Pandya Su-Exam Section Prof. Markesh Patel
SU Exam Assessement coordinator
SCET, Sarvajanik University

Dr. Hiren H. Patel Dean, SCET, Sarvajanik University 8 Traportion

Dr. Niteenbhai B. Patel Controller of Examination, Sarvajanik University

