



Sarvajanik University
Sarvajanik College of Engineering and Technology [SCET]
Bachelor of Technology in Electrical Engineering
Winter-2023 Backlog Exam Semester-III Result



Notification : SU/EXAM/ResultNotification/W2023Bklg_25012024_008

Print On Date: 25/Jan/2024

No. of Credit			2			4					5					4			4					4					0			Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Principles of Economics and Management[BTAS10301]			Applied Mathematics for Electrical Engineering[BTAS11303]					Electrical Circuit Analysis[BTEL13304]					Engineering Electromagnetics[BTEL13302]			Analog and Digital Circuit[BTEL13306]					Electrical Machine - I[BTEL13301]					Essence of Indian Traditional Knowledge[BTMD17301]								
			CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade						
1	ET21BTEL007	Jaynika Vijay Jeewa	BC	BC	BC	BC	AB	CD	CD	BC	CC	AB	CD	BC	BC	CC	CD	CC	BC	BC	CD	AA	BC	AB	AB	BC	BB	BB		CC	CC	161	23	7.00	23	0	Pass
2	ET21BTEL030	Shukla Maharshi	BB	CD	BC	CC	BC	CD	CD	CC	CC	AB	FF	BC	FF	CD	FF	FF	CC	BC	FF	BB	FF	BC	AA	BC	BB	BB		BC	BC	70	23	3.04	10	3	Fail
3	ET21BTEL032	Apurva Singh Omprakash	BC	NA	FF	CD	BC	FF	CD	FF	CD	AB	NA	BC	FF	NA	NA	FF	CD	BC	FF	AA	FF	AA	AA	CC	BC	BB		BC	BC	32	23	1.39	4	5	Fail
4	ET21BTEL036	Surani Krishaben Kantibhai	BC	BC	BC	BC	BB	CD	CD	CC	CC	BB	FF	CD	FF	BB	FF	FF	CD	BC	CC	AA	BC	AB	AA	BC	AB	AB		BC	BC	102	23	4.43	14	2	Fail
5	ET22BTEL805	PATEL JAYKUMAR CHANDRESHBHAI	BC	FF	FF	CC	BC	CD	CD	CC	CD	BB	FF	CD	FF	BC	FF	FF	CC	BC	CD	AA	BC	AB	AA	FF	BB	FF		CC	CC	52	23	2.26	8	4	Fail
6	ET22BTEL806	PATEL PARTH SANJAYBHAI	AB	CC	BB	CC	BC	CC	CD	CC	CC	BC	FF	CD	FF	CD	CD	CD	CC	BC	CD	AA	BC	AB	AA	CC	BB	BB		BB	BB	120	23	5.22	18	1	Fail
7	ET22BTEL808	Shalinkumar Patel	BC	CC	BC	BC	BB	CD	BB	BC	CD	AB	CD	CD	CC	BC	CD	CC	CC	BB	CD	AA	BC	BB	AA	BC	AB	AB		AB	AB	160	23	6.96	23	0	Pass

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Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.

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