



Sarvajanik University
Sarvajanik College of Engineering and Technology [SCET]
Bachelor of Technology In Electronics and Communication Engineering
Winter-2023 Backlog Exam Semester-III Result



Notification : SU/EXAM/ResultNotification/W2023Bklg_25012024_009

Print On Date: 25/Jan/2024

No. of Credit			4					5					3			4					4					2					0					Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Probability and Statistics for Engineering[BTAS11302]					Digital System Design[BTEC13302]					Electromagnetics[BTEC13303]			Control Systems[BTEL13305]					Electronics Circuit[BTEC13301]					Principles of Economics and Management[BTAS10301]					Essence of Indian Traditional Knowledge[BTMD17301]										
			CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade						
1	ET21BTEC011	Desai Devika Bhavin	CD	AA	CD	AB	BC	CD	BC	CD	CD	CC	BC	CD	CC	CD	CC	FF	CD	FF	BC	BC	CD	BC	BC	BC		CD		CC			BC		BC	116	22	5.27	18	1	Fail
2	ET21BTEC024	Jain Sayam Manoj	BC	AB	CD	AA	BB	CD	BC	CC	BC	BC	BB	CD	BC	CD	CC	FF	CD	FF	CC	BB	CD	BB	BC	BB		BC		BB			BC		BC	132	22	6.00	18	1	Fail
3	ET21BTEC036	Mehta Khushiben Utpalbhai	CC	CC	CD	AB	BC	CD	CC	NA	CD	FF	BC	CD	CC	CD	CC	NA	BC	FF	BC	BC	CC	BC	BC	CC		CD		CC			BB		BB	86	22	3.91	13	2	Fail
4	ET21BTEC042	Parekh Neelkumar Pragneshkumar	CD	BB	CD	BC	CC	CC	BB	NA	BB	FF	BC	NA	FF	CD	BC	NA	CD	FF	CC	BB	CD	BB	BC	CC		CD		CC			AB		AB	64	22	2.91	10	3	Fail
5	ET21BTEC045	Parmar Prema Yogeshbhai	CC	AA	CD	BB	BC	CC	AB	CD	AB	BC	BC	CC	BC	CD	BB	FF	CD	FF	BC	BB	CC	BB	BC	BB		CD		BC			BB		BB	126	22	5.73	18	1	Fail
6	ET21BTEC048	Patel Meet Bharatbhai	CC	BB	NA	BB	FF	CD	CC	NA	BC	FF	BC	NA	FF	CD	CC	NA	CD	FF	CD	BC	CD	BC	CC	CC		CD		CC			BC		BC	36	22	1.64	6	4	Fail
7	ET21BTEC052	Rai Archie Ajaykumar	CC	AB	CD	BB	BC	BC	BB	CC	BC	BC	AB	BC	BB	CC	BC	FF	CD	FF	BB	BB	CD	BB	BC	BC		BC		BC			AA		AA	129	22	5.86	18	1	Fail
8	ET22BTEC803	Tanvi Dnyaneshwar Patil	CD	BB	CD	BC	CC	CD	CC	CD	CD	CC	BC	FF	FF	CD	BC	FF	CD	FF	CC	BB	CD	BB	BC	BC		CD		CC			AA		AA	94	22	4.27	15	2	Fail

Notification : SU/EXAM/ResultNotification/W2023Bklg_25012024_009

Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.

Prepared by & Checked by,
Prof. Mukesh Patel
SU Exam OSD/Assessment Coordinator

Verified by,
Mr. Hardik Pandya,
SU-Exam Section

Dr. Hiren H. Patel
Dean/Principal
SCET, Sarvajanik University

Dr. Niteenbhai B. Patel
Controller of Examination,
Sarvajanik University

