



Sarvajanik University
Sarvajanik College of Engineering and Technology [SCET]
Bachelor of Technology In Electronics and Communication Engineering
Winter-2023 Backlog Re-Assessment Result Semester-III Reassessment Result



Notification : SU/EXAM/ResultNotification/W2023Bklg_Re-Assessment_07022024_001

Print On Date: 07/Feb/2024

No. of Credit			4					5					3			4					4					2					0					Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Probability and Statistics for Engineering[BTAS11302]					Digital System Design[BTEC13302]					Electromagnetics[BTEC13303]			Control Systems[BTEL13305]					Electronics Circuit[BTEC13301]					Principles of Economics and Management[BTAS10301]					Essence of Indian Traditional Knowledge[BTMD17301]										
			CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade						
1	ET21BTEC024	Jain Sayam Manoj	BC	AB	CD	AA	BB	CD	BC	CC	BC	BC	BB	CD	BC	CD	CC	FF	CD	FF	CC	BB	CD	BB	BC	BB		BC		BC		BC	132	22	6.00	18	1	Fail			
2	ET21BTEC052	Rai Archie Ajaykumar	CC	AB	CD	BB	BC	BC	BB	CC	BC	BC	AB	BC	BB	CC	BC	CD	CD	CC	BB	BB	CD	BB	BC	BC		BC			AA		AA	153	22	6.95	22	0	Pass		

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NOTE: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment later.


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SU Exam OSD/Assessment Coordinator


Verified by,
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