Sarvajanik University

Sarvajanik College of Engineering and Technology [SCET]
Bachelor of Technology in Electrical Engineering
Winter 2023 Backlog Exam Semester-I Reassessment Result

Notification : SU/EXAM/ResultNotification/W2023Bklg-Re-Assessment_29022024_014

Print On Date: 29/Feb/2024

Sr No	Enrollment No	Student Name	Mathematics – I[BTAS11103] (4)			Engineering Physics[BTAS11105] (3)					Basic Electrical Engineering[BTEL12108] (4)					Basic Mechanical Engineering[BTME12109] (3)					Engineering Mechanics[BTCL12113] (3)					Workshop Practice[BTGN12110] (2)			Ethics for Engineers[BTAS10102] (2)					Technical Communication[BTAS10101] (3)			Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
			CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade						
1	ET22BTEL003	Harshal Anish Kabrawala	CD	CD	CD	CD	BC	CD	BC	CC	BB	AB	CD	AB	BB	CC	BC	CD	CC	CC	CC	BC	CD	CC	CC	CC	CD	BC	AB		BB		AB	BC	CD	CC	139	21	6.62	21	0	Pass

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NOTE: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment later.

Prepared by & Checked by,
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SU Exam OSD/Assessment Coordinator

Verified by,
Mr. Hardik Pandya,
SU-Exam Section

Dr. Hiren H. Patel
Dean/Principal
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01/03/2024
Dr. Niteenbhai B. Patel
Controller of Examination,
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