

Sarvajanik University

Sarvajanik College of Engineering and Technology **Bachelor of Technology In Electronics and Communication Engineering**



Summer 2024 Semester-V Result Backlog

I	Notification : SU	U/EXAM/ResultNotification/S2024Bklg_11062024_0	05																							Print (On Date	: 11/Ju	1/2024
	No. of Credit				4				4 .			3			4				4					T					
	r Enrollment o No	Student Name	Analog and Digital Communication[BTEC13501]					Web Technology Concepts[BTCO15504]			Engineering Optimization[BTAS10501]			Digital Signal Processing and Applications[BTEC13502]				Computing[RTFC14503]			Grade	in	current sem SPI	l in l	Current sem	nt Result			
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Notification: SU/EXAM/ResultNotification/S2024Bklg 11062024 005

Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.

Prepared by & Checked by, Prof. Mukesh Patel

SU Exam OSD/Assessment Coordinator

Verified by, Mr. Hardik Pandya, SU-Exam Section

Dean/Principal SCET, Sarvajanik University

Dr. Niteenbhai B. Patel Controller of Examination, Sarvajanik University

