

Sarvajanik University

Sarvajanik College of Engineering and Technology Bachelor of Technology in Computer Engineering Summer 2024 Backlog Exam Semester-IV Result



Notification: SU/EXAM/ResultNotification/S2024Bklg. 01072024-003

Print O	Date:	01/Jul/2024	11:09	AM
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Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.

Prepared by & Checked by, Prof. Mukesh Patel

SU Exam OSD/Assessment Coordinator

Verified by, Mr. Hardik Pandya, SU-Exam Section Dr. Hiren H. Patel Dean/Principal SCET, Sarvajanik University

Dr. Niteenbhai B. Patel Controller of Examination, Sarvajanik University

