



Sarvajanik University
Sarvajanik College of Engineering and Technology
Bachelor of Technology in Computer Engineering
Winter 2024 Backlog Exam Semester-III Reassessment Result



Notification : SU/EXAM/ResultNotification/W2024Bklg_Re-Assessment_09012025-001

Print On Date: 09/Jan/2025 11:07 AM

No. of Credit			2			5					4					4					4					4					0					Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Principles of Economics and Management[BTAS10301]			Data Structures and Applications[BTCT13301]					Computer Organisation and Architecture[BTCT13302]					Microprocessor and Interfacing[BTCT13303]					Object Oriented Programming[BTCT13304]					Probability and Statistics for Engineering[BTAS11302]					Essence of Indian Traditional Knowledge[BTMD17301]										
			CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	TEE	Course Grade									
1	ET23BTC0810	Ashwani Saroch	CC	CC	CC	CC	BB	BC	BC	BC	CD	CD	CD	CC	CC	BC	BB	CD	CC	BC	CD	AB	CD	BB	BC	BC	AB	CD	BB	BC	CC	CC	155	23	6.74	23	0	Pass			

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[Signature]

Prepared by & Checked by,
Prof. Mukesh Patel
SU Exam OSD/Assessment Coordinator

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Verified by,
Mr. Hardik Pandya,
SU-Exam Section

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Dr. Hiren H. Patel
Dean/Principal
SCET, Sarvajanik University

[Signature]
09/01/2025

Dr. Niteenbhai B. Patel
Controller of Examination,
Sarvajanik University

