



**Sarvajanik University**  
Sarvajanik College of Engineering and Technology  
Bachelor of Technology In Electronics and Communication Engineering  
Winter 2024 Semester-VII Remedial Result



Notification : SU/EXAM/ResultNotification/W2024Remedial\_10022025-004

Print On Date: 10/Feb/2025 10:57 AM

No. of Credit			4					5					2			0		4							3					Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Embedded System Design[BTEC13701]					Wireless Communication[BTEC13702]					Design Project[BTEC16701]			Indian Constitution[BTMD17701]		Professional Elective-IV BTEC14701/BTEC14703 Cyber Physical Systems/Steganography and Watermarking							Open Elective-III BTCL15701/BTCL15703/BTEL15701/BTEL15702 Fundamentals of Climate Change/Smart Transportation Planning/Renewable Energy Sources/Electric and hybrid Vehicles										
			CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAP	TEP	Course Grade	TEE	Course Grade	Course Code	Course Name	CAT	CAP	TEE	TEP	Course Grade	Course Code	Course Name	CAT	TEE	Course Grade						
1	ET22BTEC801	GANDHI KESHVI HIMANSHUBHAI	BC	AB	CD	BB	BC	BC	AB	CD	BB	BC	BB	AB	AB	CD	CD	BTEC14703	Steganography and Watermarking	BB	BB	CD	BB	BC	BTCL15703	Smart Transportation Planning	CC	BC	BC	130	18	7.22	18	0	Pass

Notification : SU/EXAM/ResultNotification/W2024Remedial\_10022025-004

*[Signature]*

Prepared by & Checked by,  
Prof. Mukesh Patel  
SU Exam OSD/Assessment Coordinator

*[Signature]*

Verified by,  
Mr. Hardik Pandya,  
SU-Exam Section

*[Signature]*

Dr. Hiren H. Patel  
Dean/Principal  
SCET, Sarvajanik University

*[Signature]*  
10/02/2025

Dr. Niteenbhai B. Patel  
Controller of Examination,  
Sarvajanik University

