

Sarvajanik University

Sarvajanik College of Engineering and Technology Bachelor of Technology in Electrical Engineering-Working Professional Winter 2024 Int.Backlog Exam Semester-I Result



Notification: SU/EXAM/ResultNotification/W2024Int_Bklg_08022025-004

Print On Date: 14/Feb/2025 12:49 PM

| | No. of Credit | | | 2 | | | 5 | | | | 5 | | | 4 | | | | credits | | E | sem | t Result | |
|----|---------------|------------------------------|---|-----|-----------------|--------------------|-------|-----|--------------------------------------|-------------|-------|-----------------------------------|-----|-----------------|--------------------|--------------------------------------|-----------------|---------|-----|------|-----|-------------|------|
| Si | r Enrollment | Student Name | Principles of Economics and Management[BTAS10181] | | | Electrical Network | | | Electrical Machine - I[BTEL13182] | | | Signals and Systems[BTEL13183] | | Grade | Current sem SPI | Earned credits (in current | | | | | | | |
| | | | CAT | TEE | Course Grade | CAT | CAP 1 | TEE | LEBL | urse ade | CAT | PTEE | HEP | Course Grade | CAT | TEE | Course Grade | points | sem | | sem | DKIG | |
| 1 | ET24BTEL605 | THAKOR DHIREN SHAMBHU PRASAD | CD | CD | CD | BC | AB | FF | AB I | FF | CC BB | FF | BC | FF | BC | CC | BC | 38 | 16 | 2.38 | 6 | 2 | Fail |

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Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.

Prepared by & Checked by, Prof. Mukesh Patel

SU Exam OSD/Assessment Coordinator

Will

Verified by, Mr. Hardik Pandya, SU-Exam Section Dr. Hiren H. Patel
Dean/Principal
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Dr. Niteenhhai R. Patel

Dr. Niteenbhai B. Patel Controller of Examination, Sarvajanik University

