



Sarvajanik University

Sarvajanik College of Engineering and Technology

Bachelor of Technology In Electronics and Communication Engineering

Summer 2025 Backlog Exam Semester-IV Result



Notification : SU/EXAM/ResultNotification/ S2025Bklg_10062025-006

Print On Date: 10/Jun/2025 11:29 AM

No. of Credit			4					4					4					4					4					2			0				Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Signals and systems[BTEC13401]					VLSI Design[BTEC13404]					Microprocessor and Microcontroller[BTEC13402]					Computer Communication and Networking[BTEC13405]					Analog Electronics[BTEC13403]					Entrepreneurship and Start- up[BTAS10401]			Elective 2 BTCO18201/BTIT18203 Introduction to Cyber Security/Web Application Design									
			CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	Course Code	Course Name	TEE	Course Grade						
1	ET22BTEC020	Kaushal Dipeshkumar Painter	CD	BB	CD	AB	BC	BB	BC	CD	CC	BC	CD	CD	CC	CD	CC	CD	CD	CC	CC	CC	BC	CD	CD	CC	BC	CC	BC	BTCO18201	Introduction to Cyber Security	PP	PP	142	22	6.45	22	0	Pass	
2	ET22BTEC026	Mitul Vasoya	BC	AB	CC	BC	BC	CC	AA	CD	AB	BC	BC	CC	BC	CD	BC	BC	CC	BB	BC	AB	BC	CD	BB	BC	BB	BC	BB	BTIT18203	Web Application Design	PP	PP	156	22	7.09	22	0	Pass	
3	ET22BTEC030	Navadiya Harshilkumar Dharmeshbhai	CD	BB	CD	BB	CC	CD	FF	CD	CC	FF	CD	CD	CD	CD	CD	CC	CC	BC	CC	CC	BC	NA	CD	FF	CC	CC	CC	BTIT18203	Web Application Design	PP	PP	80	22	3.64	14	2	Fail	
4	ET22BTEC059	Suthar Devendrakumar Viraram	CD	AB	CD	AB	BC	BB	AA	CD	CC	BC	BB	CD	BC	CD	BC	BB	BB	CD	BB	BC	BC	CC	CC	AB	BC	BC	BB	BB	BTCO18201	Introduction to Cyber Security	PP	PP	156	22	7.09	22	0	Pass

Notification : SU/EXAM/ResultNotification/ S2025Bklg_10062025-006

Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.

Prepared by & Checked by,
Prof. Mukesh Patel
SU Exam OSD/Assessment Coordinator

Verified by,
Mr. Hardik Pandya,
SU-Exam Section

Dr. Hiren H. Patel
Dean/Principal
SCET, Sarvajanik University

Dr. Niteenbhai B. Patel
Controller of Examination,
Sarvajanik University

