



Sarvajanik University
Sarvajanik College of Engineering and Technology
Master of Computer Applications
Winter 2025 Backlog Exam Semester-IV Result



Notification : SU/EXAM/ResultNotification/W2025Bklg_17122025-005

Print On Date: 17/Dec/2025 11:35 AM

| No. of Credit | | | 15 | | | Current Sem Grade points | Total credits in current sem | Current sem SPI | Earned credits in current sem | Current sem bklg | Result |
|---------------|---------------|---------------------|--------------------|-----|--------------|--------------------------|------------------------------|-----------------|-------------------------------|------------------|--------|
| Sr No | Enrollment No | Student Name | Project[MTCA16401] | | | | | | | | |
| | | | CAP | TEP | Course Grade | | | | | | |
| 1 | ET23MTCA030 | MISHRA AKRUTI SHREE | CC | BB | BB | 120 | 15 | 8.00 | 15 | 0 | Pass |

Notification : SU/EXAM/ResultNotification/W2025Bklg_17122025-005

Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.

Prepared by & Checked by,
Dr. Dhruti Sharma
SU Exam OSD/Assessment Coordinator

Verified by,
Mr. Nimesh Desai,
SU-Exam Section

Dr. Hiren H. Patel
Dean/Principal
SCET, Sarvajanik University

Dr. Nilesh Pandya
Controller of Examination,
Sarvajanik University

