



Sarvajanik University
Sarvajanik College of Engineering and Technology
Bachelor of Technology in Electrical Engineering
Winter 2025 Backlog Exam Semester-V Result



Notification : SU/EXAM/ResultNotification/W2025Bklg_07012026-003 Print On Date: 07/Jan/2026 10:47 AM

No. of Credit			5					4					5					3			4										Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
Sr No	Enrollment No	Student Name	Microprocessor and Microcontroller[BTEL13501]					Power System- II[BTEL13502]					Power Electronics[BTEL13503]					Engineering Optimization[BTAS10501]			OE-I BTCO15504/BTEC15504/BTEC15501 Web Technology Concepts/Robotics and Automation/Internet of Things															
			CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	Course Code	Course Name	CAT	CAP	TEE	TEP	Course Grade									
1	ET21BTEL007	Jaynika Vijay Jeewa	BC	AB	CD	CC	BC	BC	BC	CD	CC	CC	CD	CC	CD	CC	CC	BC	CD	CC	BTEC15504	Robotics and Automation	BC	CC	CD	BC	BC	135	21	6.43	21	0	Pass			
2	ET21BTEL023	Patel Noaman Salimbhai	BC	AB	CD	CD	BC	CC	BC	CD	CD	CC	CD	BC	CD	CC	CC	BC	CC	BC	BTEC15501	Internet of Things	CD	CC	CD	CC	CC	134	21	6.38	21	0	Pass			
3	ET22BTEL003	Harshal Anish Kabrawala	BC	AB	CD	CD	BC	CC	BC	CD	CC	CC	CD	BB	CD	CC	CC	CD	CD	CD	BTCO15504	Web Technology Concepts	CC	BB	CD	CD	CC	128	21	6.10	21	0	Pass			
4	ET22BTEL014	Verma Jigarkumar Dharmendrakumar	BC	AB	CD	CD	BC	CC	BC	CD	CD	CC	CC	BC	CD	CD	CC	CC	CC	CC	BTCO15504	Web Technology Concepts	CD	CC	CC	CC	CC	131	21	6.24	21	0	Pass			
5	ET23BTEL801	Amit Kumar	BC	AA	CD	CD	BC	CC	BB	CD	BC	BC	CD	BB	CD	CD	CC	CD	CD	CD	BTCO15504	Web Technology Concepts	CC	BB	CD	CC	CC	132	21	6.29	21	0	Pass			

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Note: 1) Kindly refer detailed guidelines for Rechecking/Reassessment process. 2) This Backlog exam result may be affected due to Re-Assessment. Final result will be declared after reassessment.


Prepared by & Checked by,
Dr. Dhruvi Sharma
SU Exam OSD/Assessment Coordinator


Verified by,
Mr. Nimesh Desai,
SU-Exam Section


Dr. Hiren H. Patel
Dean/Principal
SCET, Sarvajanik University


Dr. Nilesh Pandya
Controller of Examination,
Sarvajanik University

