Sarvajanik University

Sarvajanik College of Engineering and Technology

Bachelor of Technology in Electronics and Communication Engineering

Winter 2025 Backlog Exam After Bklg_Re-assessment Semester-V Final Result

Notification : SU/EXAM/FinalResultNotification/ W2025Bklg_26022026-010

Print On Date: 26/Feb/2026 11:58 AM

Sr No	Enrollment No	Student Name	Engineering Optimization[BTAS10501] (3)			Analog and Digital Communication[BTEC13501] (4)					Digital Signal Processing and Applications[BTEC13502] (4)					PE-I BTEC14502/BTEC14501 Professional Elective I (Computer organisation and RTL design)/Advance Microcontroller (4)							OE-I BTCL15503/BTCO15504 Sustainable Energy System/Web Technology Concepts (4)							Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result	Backlogs	Cumulative Total Credits	Cumulative Earned Credits	Cumulative Grade Points	CPI
			CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	Course Code	Course Name	CAT	CAP	TEE	TEP	Course Grade	Course Code	Course Name	CAT	CAP	TEE	TEP	Course Grade											
1	ET22BTEC026	Mitul Vasoya	CD	CC	CC	CD	CC	FF	BB	FF	BC	CC	CD	CC	CC	BTEC14502	Professional Elective I (Computer organisation and RTL design)	BC	BC	FF	CD	FF	BTCL15503	Sustainable Energy System	BC	AB	CC	AB	BB	74	19	3.89	11	2	Fail	S5: 2	145	137	941	6.49
2	ET22BTEC034	Patil Bhavik Suresh	CD	BC	BC	BC	BC	CD	CC	CC	BC	CC	BB	BC	BB	BTEC14501	Advance Microcontroller	CC	CC	BC	CC	BC	BTCO15504	Web Technology Concepts	CC	BB	BC	CC	BC	133	19	7.00	19	0	Pass	S6: 1	145	145	1008	6.95
3	ET22BTEC803	PATIL TANVI DNYANESHWAR	CD	CD	CD	CD	BB	CD	BB	CC	CC	BB	CC	CD	BC	BTEC14502	Professional Elective I (Computer organisation and RTL design)	CD	BC	CD	CD	CC	BTCO15504	Web Technology Concepts	CC	BB	CD	CC	CC	115	19	6.05	19	0	Pass	S6: 1, S7: 2	103	96	592	5.75

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Prepared by & Checked by,
Dr. Dhruti Sharma
SU Exam OSD/Assessment Coordinator

Verified by,
Mr. Nimesh Desai,
SU-Exam Section

Dr. Hiren H. Patel
Dean/Principal,
SCET, Sarvajanik University

Dr. Nilesh Pandya
Controller of Examination,
Sarvajanik University