



**Sarvajanik University**  
Sarvajanik College of Engineering and Technology  
Bachelor of Technology in Electronics and Communication Engineering  
Winter 2025 Interim Backlog Exam Semester-III Reassessment Result



Notification : SU/EXAM/ResultNotification/W2025Int\_Bklg\_Re-Assessment\_09032026-005

Print On Date: 09/Mar/2026 11:25 AM

Sr No	Enrollment No	Student Name	2			4					4					5					3			4					0		Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
			Principles of Economics and Management[BTAS10301]			Probability and Statistics for Engineering[BTAS11302]					Electronics Circuit[BTEC13301]					Digital System Design[BTEC13302]					Electromagnetics[BTEC13303]			Control Systems[BTEL13305]					Essence of Indian Traditional Knowledge[BTMD17301]							
			CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	TEE	Course Grade						
1	ET24BTEC001	ARYAN MUKESHBHAI PATEL	AB	FF	FF	CD	AA	FF	AA	FF	CD	BC	CD	CC	CC	CC	BB	CC	CC	BC	BC	BC	BC	CC	CD	CD	CC	CC	CC	104	22	4.73	16	2	Fail	
2	ET24BTEC018	GHORI HARSHAL GHANSHYAMBHAI	AB	CD	BC	BC	AB	BB	AB	AB	BC	AA	CD	CC	BC	BC	AB	CD	BB	BC	BC	CC	BC	BB	CD	BC	BC	BB	BB	162	22	7.36	22	0	Pass	

Notification : SU/EXAM/ResultNotification/W2025Int\_Bklg\_Re-Assessment\_09032026-005

Prepared by & Checked by,  
Dr. Dhruvi Sharma  
SU Exam OSD/Assessment Coordinator

Verified by,  
Mr. Nimesh Desai,  
SU-Exam Section

Dr. Hiren H. Patel  
Dean/Principal  
SCET, Sarvajanik University

Dr. Nilesh Pandya  
Controller of Examination,  
Sarvajanik University

