



Sarvajanik University
Sarvajanik College of Engineering and Technology
Bachelor of Technology in Electrical Engineering
Winter 2025 Interim Backlog Exam Semester-III Reassessment Result



Notification : SU/EXAM/ResultNotification/W2025Int_Bklg_Re-Assessment_09032026-004

Print On Date: 09/Mar/2026 11:22 AM

Sr No	Enrollment No	Student Name	2			4					5					4			4					0		4					Current Sem Grade points	Total credits in current sem	Current sem SPI	Earned credits in current sem	Current sem bklg	Result
			Principles of Economics and Management[BTAS10301]			Applied Mathematics for Electrical Engineering[BTAS11303]					Electrical Circuit Analysis[BTEL13304]					Engineering Electromagnetics[BTEL13302]			Analog and Digital Circuit[BTEL13306]					Essence of Indian Traditional Knowledge[BTMD17301]		Electrical Machine - I[BTEL13301]										
			CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	CAP	TEE	TEP	Course Grade	CAT	TEE	Course Grade	CAT	CAP	TEE	TEP	Course Grade	TEE	Course Grade	TEE	Course Grade	CAT	CAP	TEE						
1	ET24BTEL016	MENDPARA MEET SURESH	BB	BB	BB	CD	AB	CD	BB	BC	BC	BB	CD	CD	CC	CD	CD	CD	BC	AB	CD	CD	BC	AB	AB	BB	AA	FF	CD	FF	122	23	5.30	19	1	Fail
2	ET24BTEL032	VEER SHAH	BC	BC	BC	CC	BC	CD	BB	BC	CC	BC	FF	CC	FF	CD	FF	FF	CD	BC	FF	FF	FF	CC	CC	BC	AB	FF	CD	FF	42	23	1.83	6	4	Fail

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